T0022T "ET222907

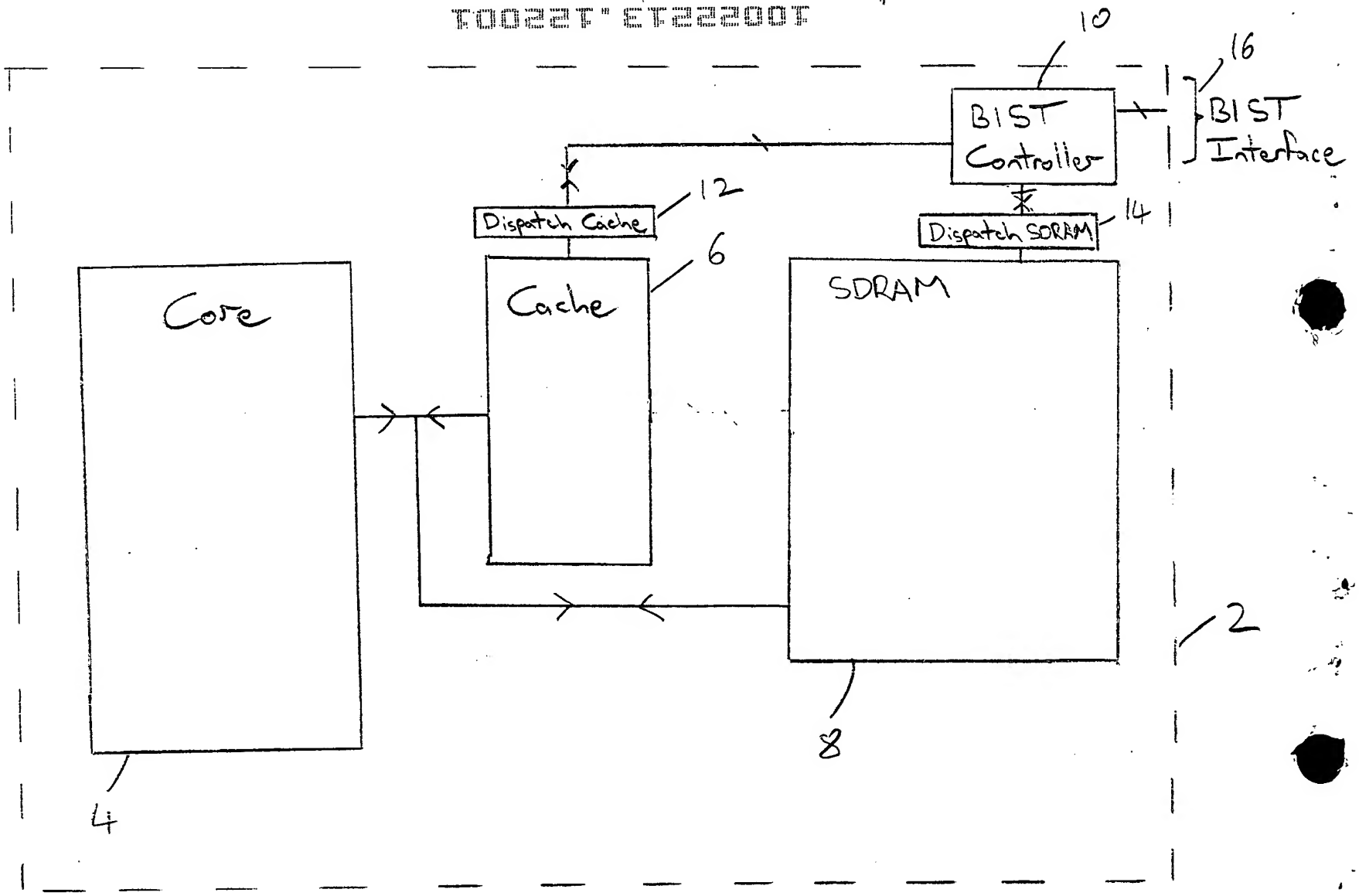

Fig. 1

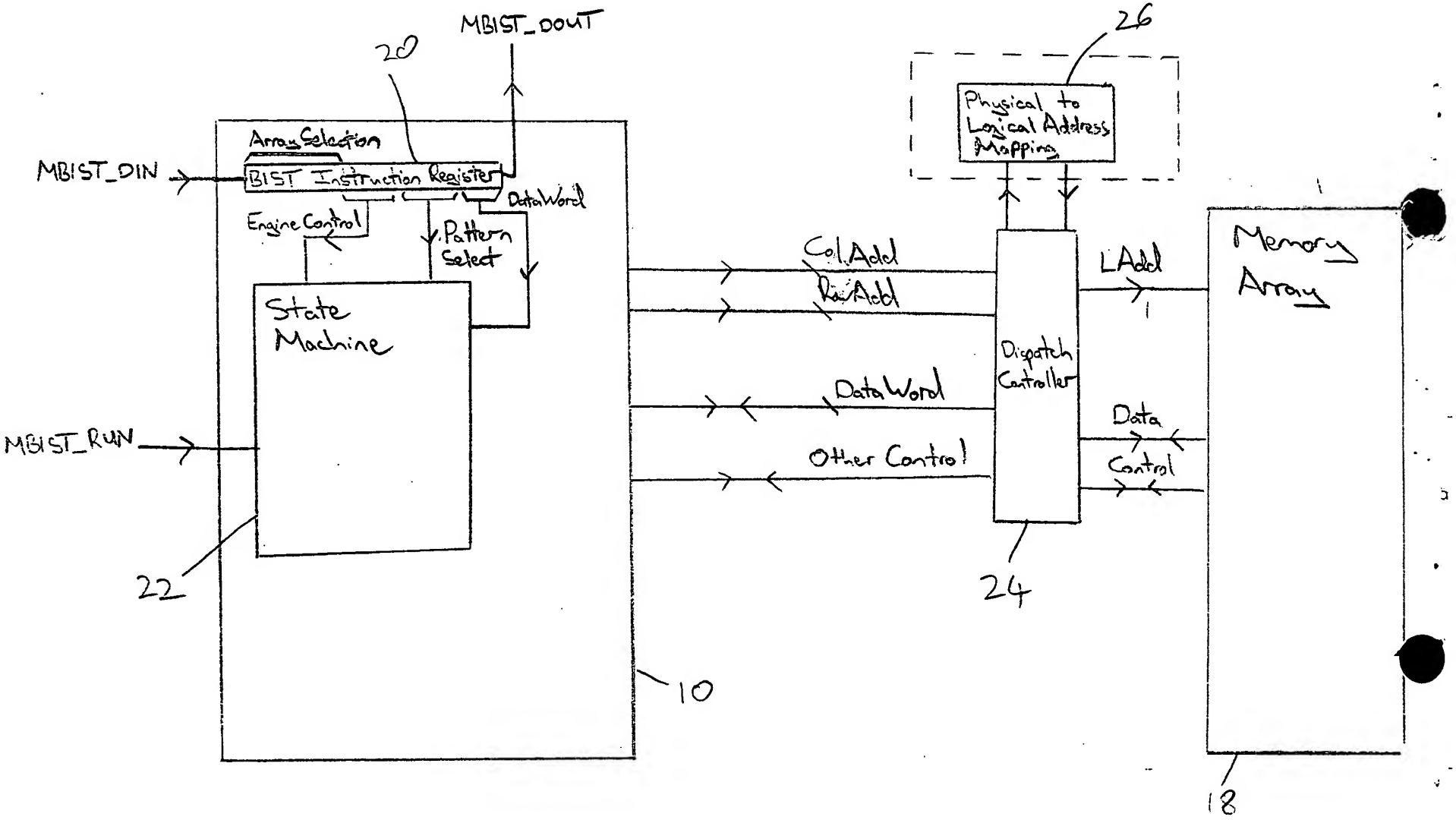
MBIST_DOUT
20
26
Physical to Logical Address Mapping
Array Selection
MBIST_DIN
BIST Instruction Register
DataWord
Engine Control
Pattern Select
Col Addl
Row Addl
LAddl
Memory Array
State Machine
Dispatch Controller
DataWord
Data
MBIST_RUN
Other Control
Control
22
24
10
18

Fig. 2

Docket: 1222-1204

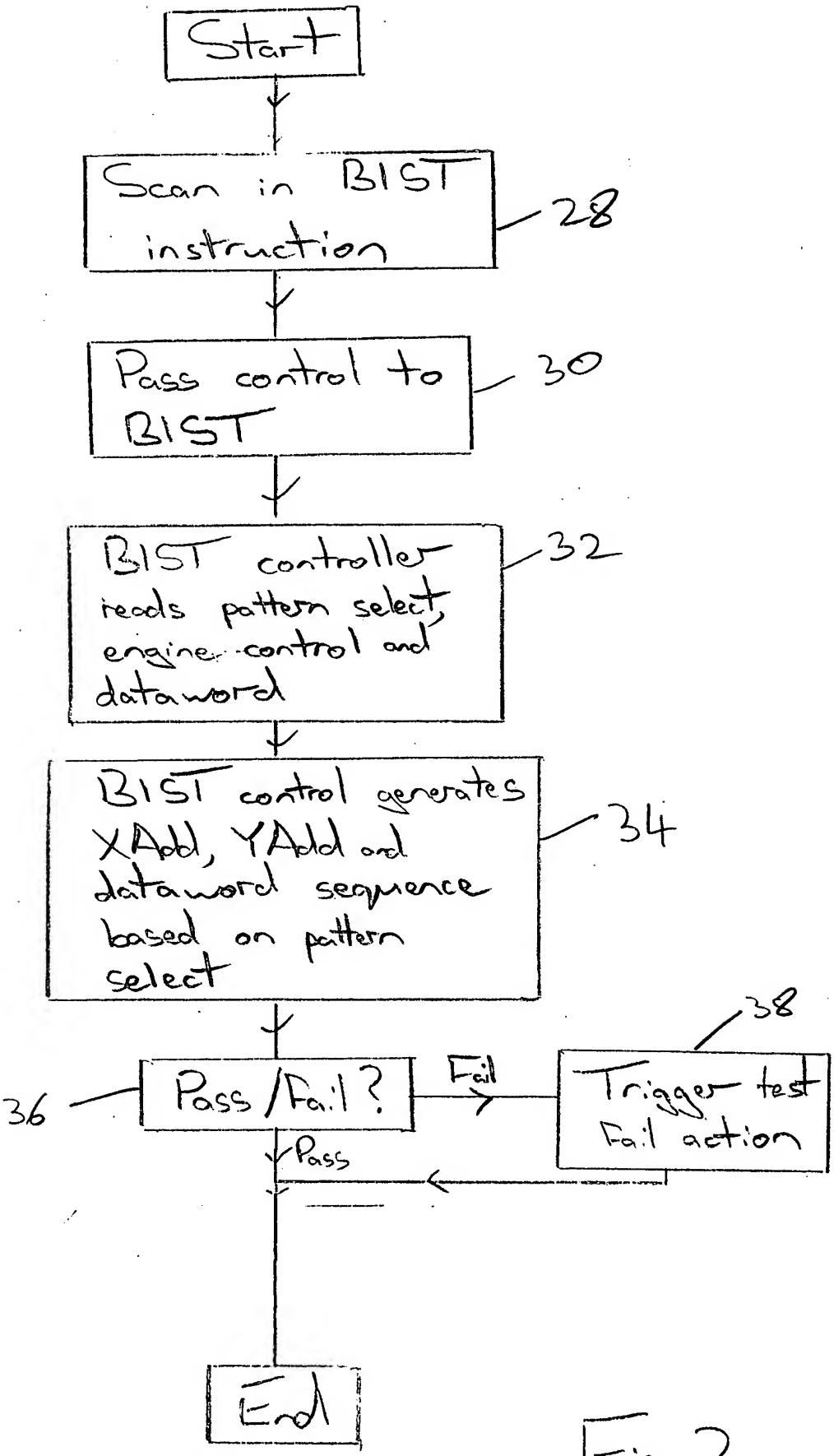

Fig. 3

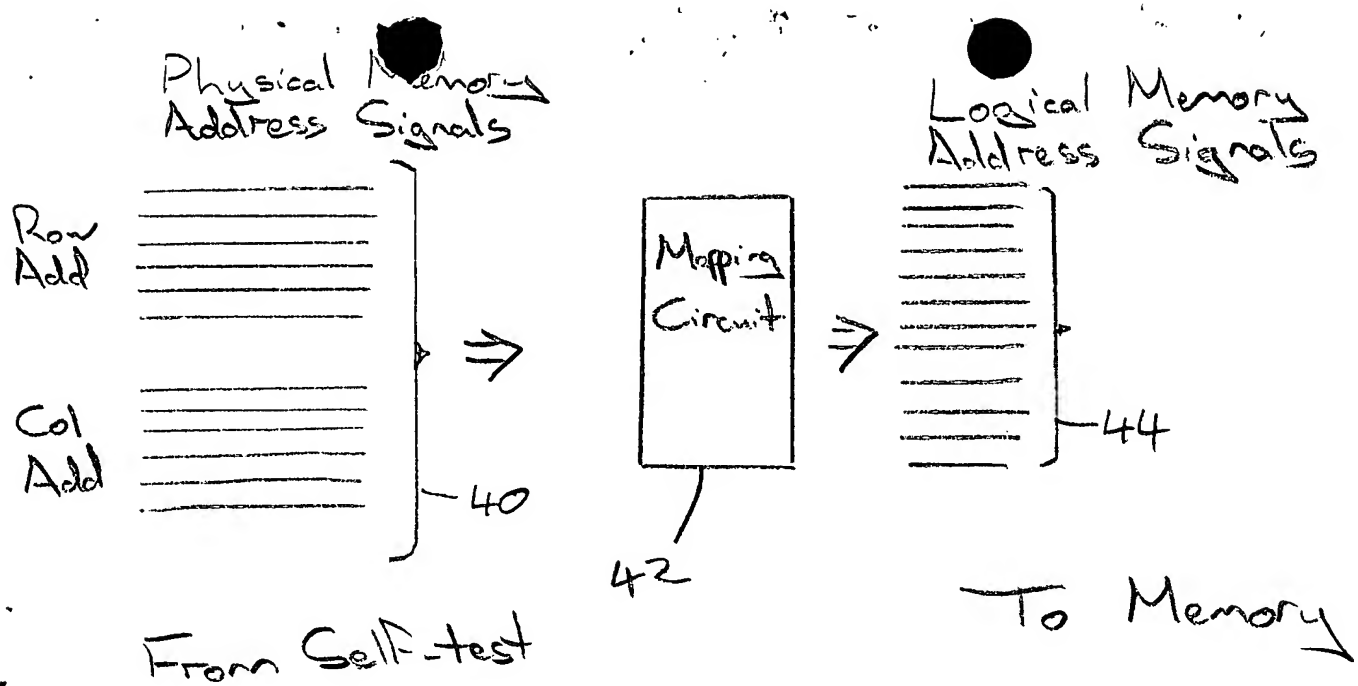

Fig. 4

FOUNDATION-FOUND01

LA [8:0] when implemented has 32 rows, 8 cols, 1 block select

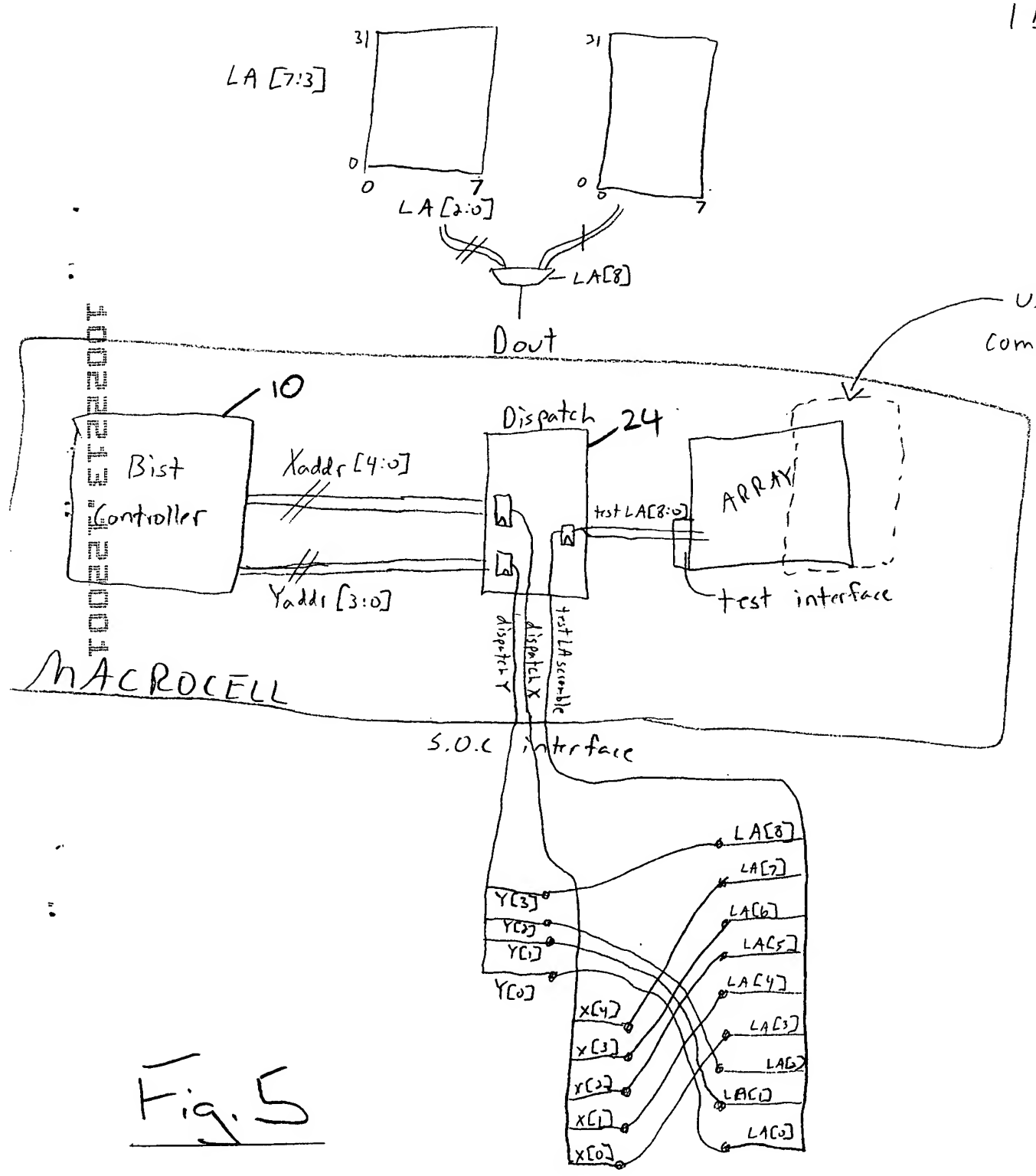

Fig. 5